

Engineer – Layout

Ho Chi Minh City,
SHTP, Dist.9, HCMC

Company Description

Microchip Technology Inc. is a leading provider of embedded control applications. Our product portfolio comprises general purpose and specialized 8-bit, 16-bit, and 32-bit microcontrollers, 32-bit microprocessors, field-programmable gate array (FPGA) products, a broad spectrum of high-performance linear, mixed-signal, power management, thermal management, radio frequency (RF), timing, safety, security, wired connectivity and wireless connectivity devices, as well as serial Electrically Erasable Programmable Read Only Memory (EEPROM), Serial Flash memories, Parallel Flash memories, and serial Static Random Access Memory (SRAM). We also license Flash-IP solutions that are incorporated in a broad range of products.

Job Description

- Responsible for cell layout, block level layout, macro planning and interconnect.
- Compact layout is essential.
- Working under designer's supervision to keep schedule and quality intact.

Job Requirements

- 1+ year's analog/memory layout experience
- BS or MS in Electrical Engineering. Semiconductor courses preferred
- Hands-on 1+ years plus in Semiconductor Integrated Circuit Layout Development
- Fluency in Unix, other programming language
- Fluency in mainstream CAD layout tools, skill works
- Fluency in Vietnamese, English, others are plus
- Professional ethic, team works
- No restriction to any country with Microchip's presence
- Special skill in CAD system and IT is plus to support the layout team while the team size is small

Please feel free to contact and send your CV via:

Si: siletiens@gmail.com